Form A2-3: Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99. The form can be used in a variety of ways as detailed below:

1. To obtain **Fully Type Tested** status (≤ 50 kW)

   The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register. Tests 1 – 15 must all be completed and compliant for the **Power Generating Module** to be classified as **Fully Type Tested**.

2. To obtain **Type Tested** status for a product

   This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register.

   Where the **Manufacturer** is seeking to obtain **Type Tested** status for an **Interface Protection** device the appropriate section of Form A2-4 should be used.

3. One-off Installation

   This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the

Interface Protection is to be demonstrated on site.
Note:

Within this Form A2-3 the term **Power Park Module** will be used but its meaning can be interpreted within Form A2-3 to mean **Power Park Module**, **Generating Unit or Inverter** as appropriate for the context.

However, note that compliance shall be demonstrated at the **Power Park Module** level.

If the **Power Generating Module** is **Fully Type Tested** and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3-1 or A3-2) should include the **Manufacturer's** reference number (the system reference), and this form does not need to be submitted.

Where the **Power Generating Module** is not registered with the ENA Type Test Verification Report Register or is not **Fully Type Tested** this form (all or in parts as applicable) needs to be completed and provided to the **DNO**, to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99.

Manufacturer's reference number		ERD-CR202308005	
PGM technology		S6-GR1P7K2,S6-GR1P8K2	
Manufacturer name		Ginlong Technologies Co., Ltd.	
Address		No. 57 Jintong Road, Seafront (Binhai) Industrial Park, Xiangshan, Ningbo, Zhejiang,315712,P.R.China	
Tel	(+86) 574 6580 3377	Web site	www.ginlong.com
E:mail	ruyi.Pan@ginlong.com		
Registered Capacity		7kVA/8kVA	
Energy storage capacity for **Electricity Storage** devices		kWh	

There are four options for Testing: (1) **Fully Type Tested**(≤ 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28.

Insert Document reference(s) for **Manufacturers' Information**

Tested option:	**1. Fully Type Tested**	**2. Type Tested product**	**3. One-off Manufacturers' Info.**	**4. Tested on Site at time of Commissioning**
0. **Fully Type Tested** - all tests detailed below completed and evidence attached to this submission		**N/A**	**N/A**	**N/A**
1. Operating Range	**N/A**	√		
2. PQ – Harmonics		√		
3. PQ – Voltage Fluctuation and Flicker		√		
4. PQ – DC Injection (**Power Park Module**s only)		√		
5. **Power Factor** (PF)		√		
6. Frequency protection trip and ride through tests		√		
7. Voltage protection trip and ride through tests		√		
8. Protection – Loss of Mains Test, Vector Shift and RoCoF Stability Test		√		
9. **LFSM-O** Test		√		
10. Protection – Reconnection Timer		√		
11. Fault Level Contribution		√		

There are four options for Testing: (1) **Fully Type Tested**(≤ 50 kW), (2) **Type Tested** product, (3) one-off installation, (4) tested on site at time of commissioning. The check box below indicates which tests in this Form have been completed for each of the options. With the exception of **Fully Type Tested PGM**s tests may be carried out at the time of commissioning (Form A4). **Type Tested** status is suitable for devices > 50 kW where the power quality aspects need consideration on a site by site basis in accordance with EREC G5 and EREC P28.

Insert Document reference(s) for **Manufacturers' Information**

Tested option:	**1. Fully Type Tested**	**2. Type Tested product**	**3. One-off Manufacturers' Info.**	**4. Tested on Site at time of Commissioning**
12. Self-monitoring Solid State Switch		**N/A**		
13. Wiring functional tests if required by para 15.2.1 (attach relevant schedule of tests)		**N/A**		
14. Logic Interface (input port)		√		
15. Cyber security		√		

Manufacturer compliance declaration. - I certify that all products supplied by the company with the above **Type Tested Manufacturer**'s reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site **Modification**s are required to ensure that the product meets all the requirements of EREC G99.

Signed	1.Feb.2024	On behalf of	Ginlong Technologies Co., Ltd.

Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the Manufacturer then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.

A2-3 Compliance Verification Report –Tests for Type A Inverter Connected Power Generating Modules – test record

1. Operating Range: Tests should be carried with the **Power Generating Module** operating at **Registered Capacity** and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The **Interface Protection** shall be disabled during the tests.

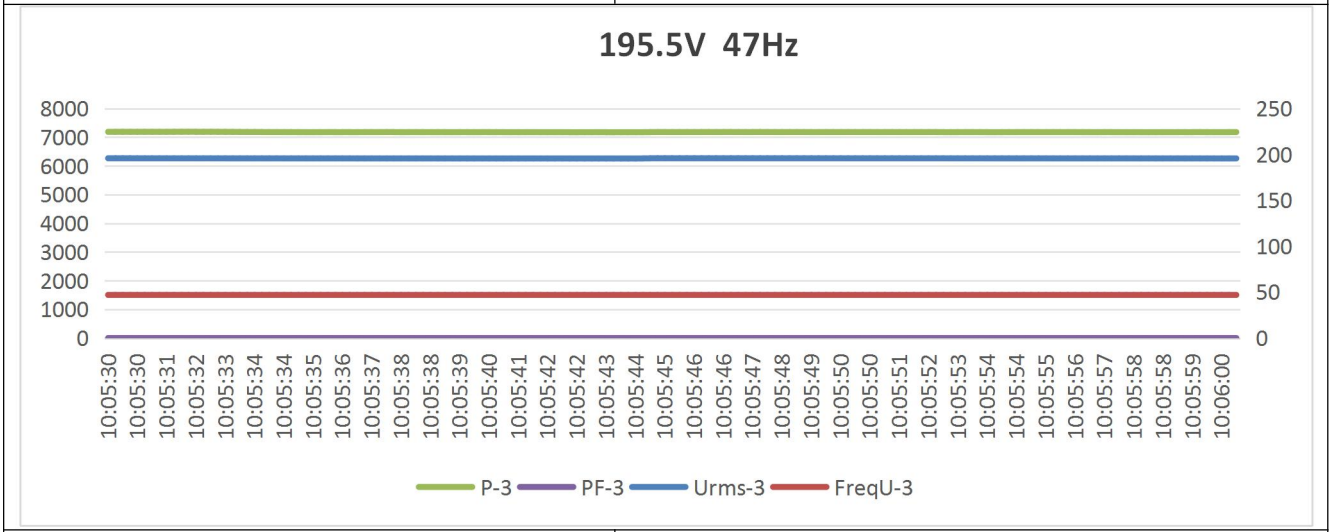
In case of a PV **Power Park Module** the PV primary source may be replaced by a DC source.

In case of a full converter **Power Park Module** (e.g wind) the primary source and the prime mover I**nverter**/rectifier may be replaced by a DC source.

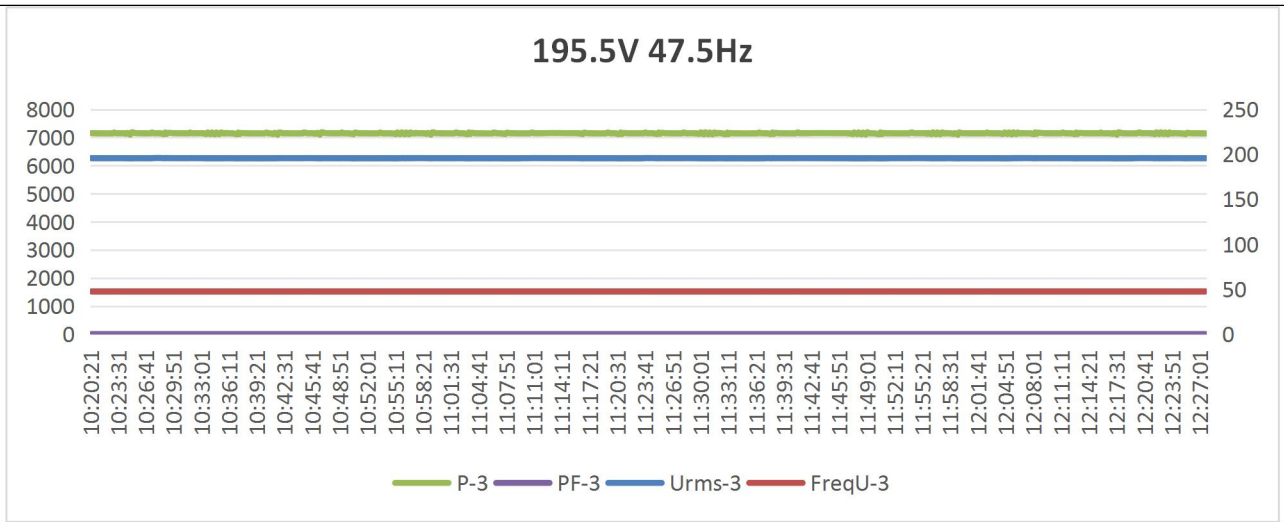
Pass or failure of the test should be indicated in the fields below (right hand side), for example with the statement "Pass", "No disconnection occurs", etc. Graphical evidence is preferred.

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.

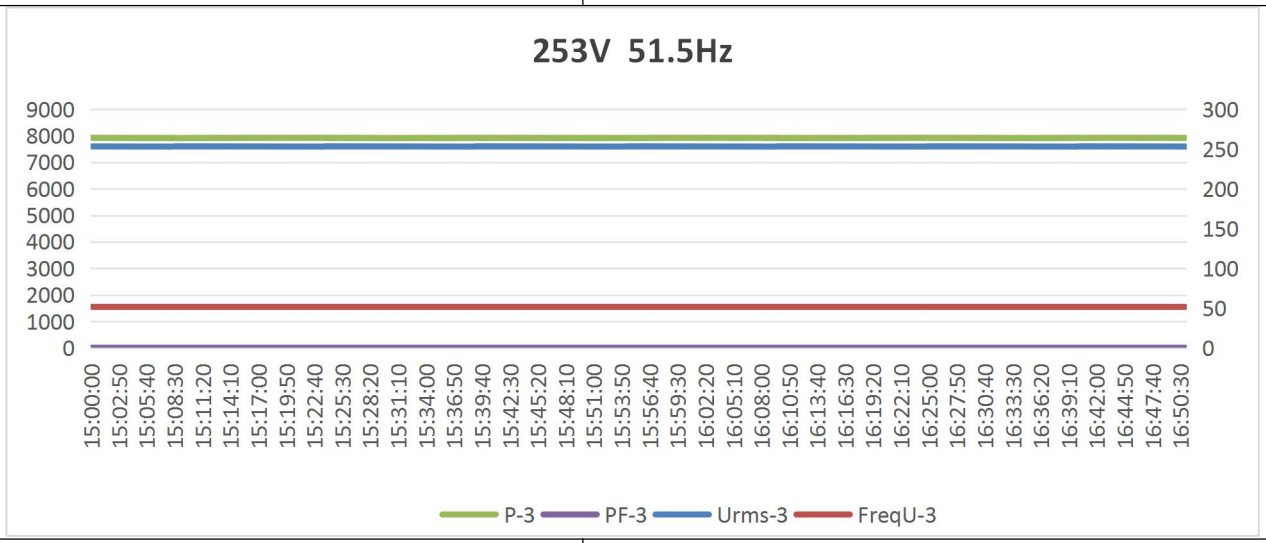
Test 1 Voltage = 85% of nominal (195.5 V), Frequency = 47 Hz, Power Factor = 1, Period of test 20 s	No disconnection occurs. Pass.



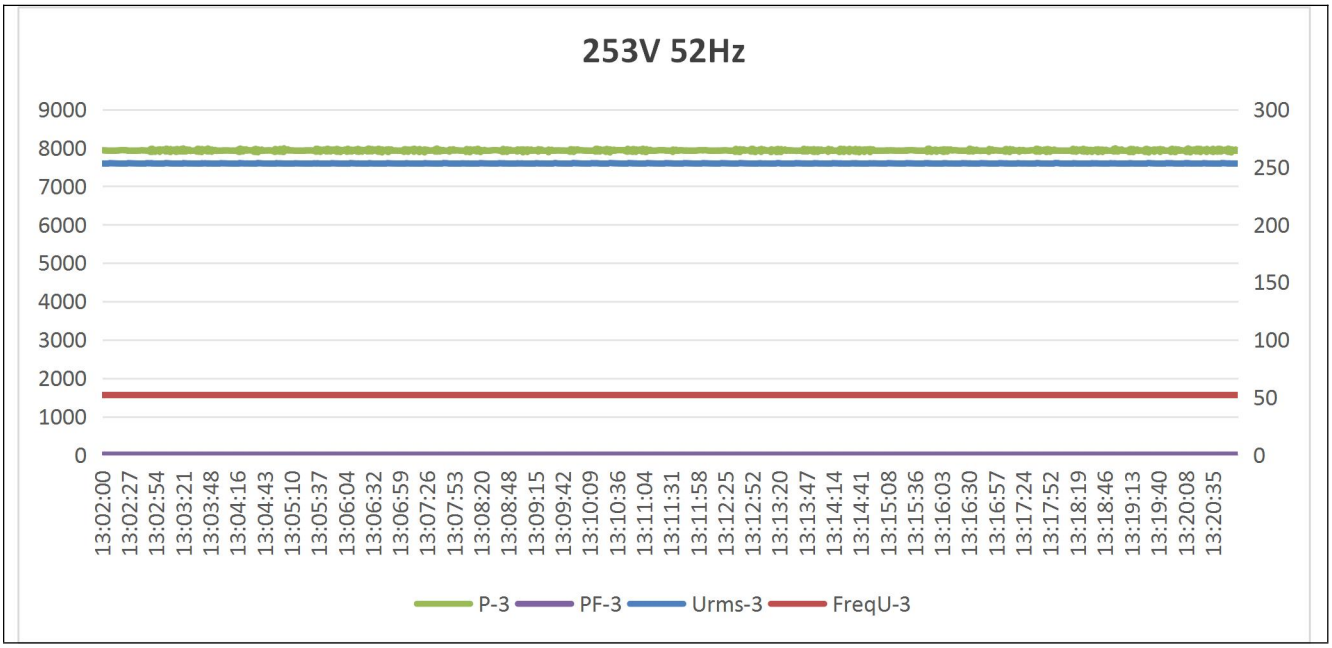
Test 2 Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, Power Factor = 1, Period of test 90 minutes	No disconnection occurs. Pass.



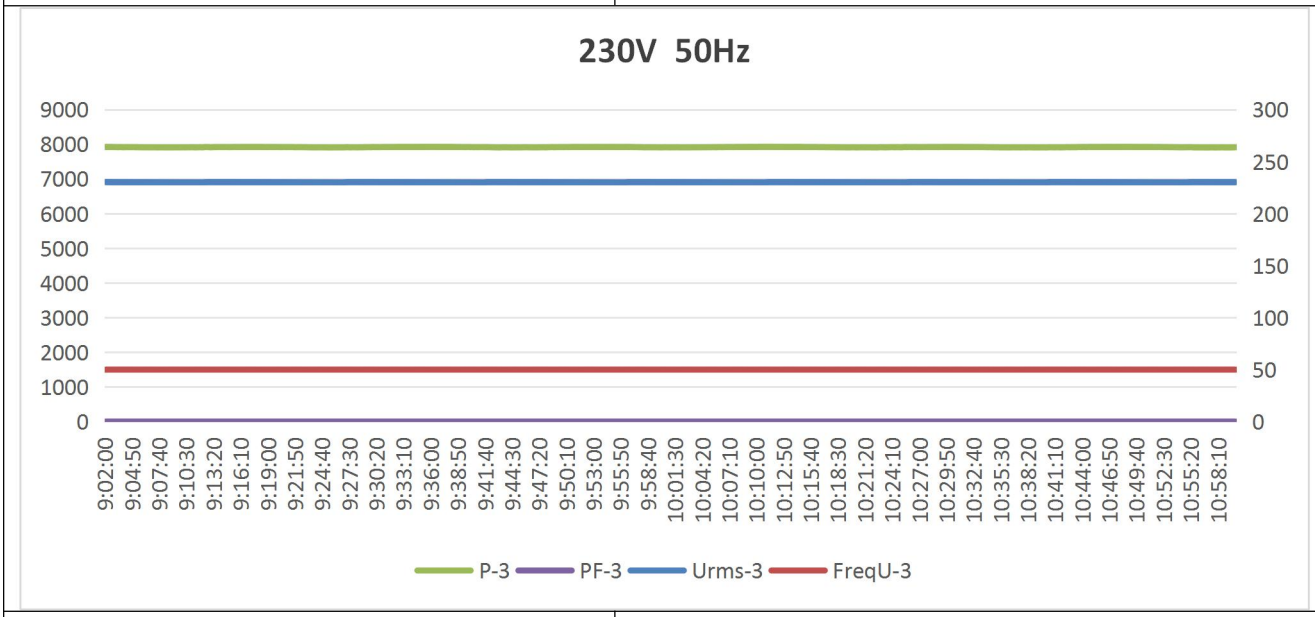
Test 3 Voltage = 110% of nominal (253 V)., Frequency = 51.5 Hz, Power Factor = 1, Period of test 90 minutes	No disconnection occurs. Pass.



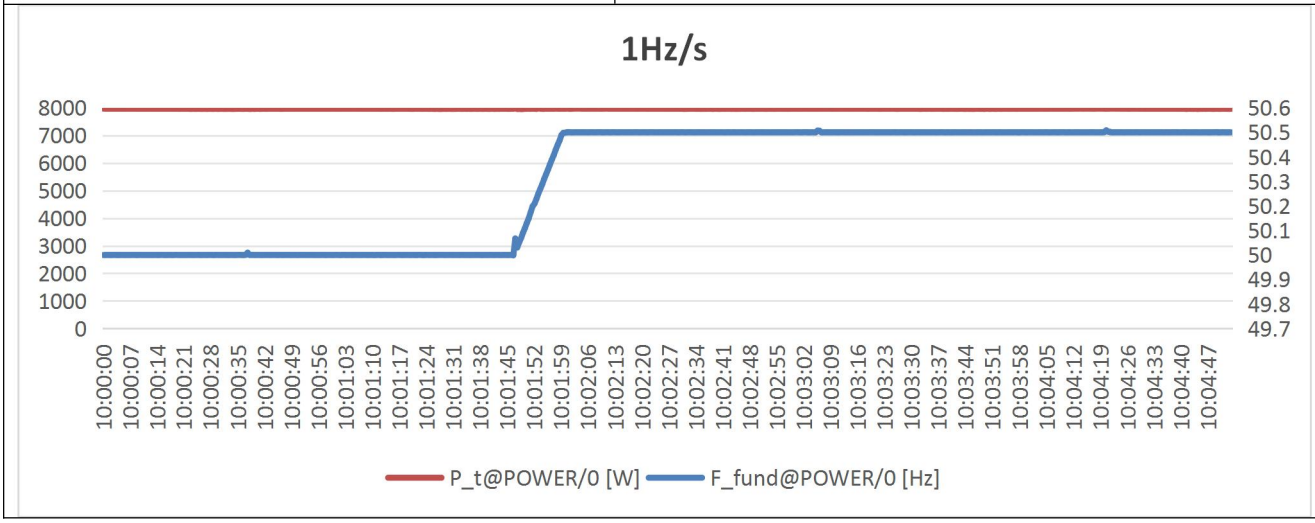
Test 4 Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power Factor = 1, Period of test 15 minutes	No disconnection occurs. Pass.


253V 52Hz
9000
8000
7000
6000
5000
4000
3000
2000
1000
0
300
250
200
150
100
50
0
13:02:00
13:02:27
13:02:54
13:03:21
13:03:48
13:04:16
13:04:43
13:05:10
13:05:37
13:06:04
13:06:32
13:06:59
13:07:26
13:07:53
13:08:20
13:08:48
13:09:15
13:09:42
13:10:09
13:10:36
13:11:04
13:11:31
13:11:58
13:12:25
13:12:52
13:13:20
13:13:47
13:14:14
13:14:41
13:15:08
13:15:36
13:16:03
13:16:30
13:16:57
13:17:24
13:17:52
13:18:19
13:18:46
13:19:13
13:19:40
13:20:08
13:20:35
P-3
PF-3
Urms-3
FreqU-3

Test 5 Voltage = 100% of nominal (230 V), Frequency = 50.0 Hz, **Power Factor** = 1, Period of test = 90 minutes	No disconnection occurs. Pass.



Test 6 RoCoF withstand Confirm that the **Power Generating Module** is capable of staying connected to the **Distribution Network** and operate at rates of change of frequency up to 1 Hzs^{-1} as measured over a period of 500 ms. Note that this is not expected to be demonstrated on site.	No disconnection occurs. Pass.



2. Power Quality – Harmonics:

For **Power Generating Module**s of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12, and measurements for the 2^{nd} – 13^{th} harmonics should be provided. The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment. For three phase **Power Generating Module**s, measurements for all phases should be provided.

For **Power Generating Module**s of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.

The rating of the **Power Generating Module** (per phase) should be provided below, and the Total Harmonic Distortion (THD) and Partial Weighted Harmonic Distortion (PWHD) should be provided at the bottom of this section.

Power Generating Module tested to BS EN 61000-3-12

Power Generating Module rating per phase (rpp)				8		kVA	Harmonic % = Measured Value (A) x 23/rating per phase (kVA)	
Single or three phase measurements (for single phase measurements, only complete L1 columns below).				Single				
Harmonic	At 45-55% of **Registered Capacity**						Limit in BS EN 61000-3-12	
	Measured Value (MV) in Amps			Measured Value (MV) in %				
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.1726	--	--	0.4962	--	--	8%	8%
3	0.4834	--	--	1.3898	--	--	21.6%	Not stated

4	0.0149	--	--	0.0428	--	--	4%	4%
5	0.3424	--	--	0.9844	--	--	10.7%	10.7%
6	0.0276	--	--	0.0794	--	--	2.67%	2.67%
7	0.2797	--	--	0.8041	--	--	7.2%	7.2%
8	0.0185	--	--	0.0532	--	--	2%	2%
9	0.1575	--	--	0.4528	--	--	3.8%	Not stated
10	0.0118	--	--	0.0339	--	--	1.6%	1.6%
11	0.1231	--	--	0.3539	--	--	3.1%	3.1%
12	0.0083	--	--	0.0239	--	--	1.33%	1.33%
13	0.0712	--	--	0.2047	--	--	2%	2%
14	0.0074	--	--	0.0213	--	--	--	--
15	0.0563	--	--	0.1619	--	--	--	--
16	0.0105	--	--	0.0302	--	--	--	--
17	0.0414	--	--	0.1190	--	--	--	--
18	0.0094	--	--	0.0270	--	--	--	--
19	0.0368	--	--	0.1058	--	--	--	--
20	0.0073	--	--	0.0210	--	--	--	--
21	0.0337	--	--	0.0969	--	--	--	--
22	0.0080	--	--	0.0230	--	--	--	--
23	0.0329	--	--	0.0946	--	--	--	--
24	0.0079	--	--	0.0227	--	--	--	--
25	0.0309	--	--	0.0888	--	--	--	--
26	0.0061	--	--	0.0175	--	--	--	--
27	0.0282	--	--	0.0811	--	--	--	--
28	0.0061	--	--	0.0175	--	--	--	--
29	0.0257	--	--	0.0739	--	--	--	--
30	0.0078	--	--	0.0224	--	--	--	--
31	0.0227	--	--	0.0653	--	--	--	--
32	0.0070	--	--	0.0201	--	--	--	--

33	0.0204	--	--	0.0587	--	--	--	--
34	0.0070	--	--	0.0201	--	--	--	--
35	0.0176	--	--	0.0506	--	--	--	--
36	0.0077	--	--	0.0221	--	--	--	--
37	0.0127	--	--	0.0365	--	--	--	--
38	0.0091	--	--	0.0262	--	--	--	--
39	0.0107	--	--	0.0308	--	--	--	--
40	0.0099	--	--	0.0285	--	--	--	--
THD[27]	--	--	--	2.07	--	--	23%	13%
PWHD[28]	--	--	--	4.68	--	--	23%	22%
Harmonic	At 100% of **Registered Capacity**						Limit in BS EN 61000-3-12	
	Measured value (MV) in Amps			Measured value (MV) in %				
	L1	L2	L3	L1	L2	L3	1 phase	3 phase
2	0.2873	--	--	0.8260	--	--	8%	8%
3	0.7160	--	--	2.0585	--	--	21.6%	Not stated
4	0.0531	--	--	0.1527	--	--	4%	4%
5	0.5989	--	--	1.7218	--	--	10.7%	10.7%
6	0.0548	--	--	0.1575	--	--	2.67%	2.67%
7	0.4317	--	--	1.2411	--	--	7.2%	7.2%
8	0.0381	--	--	0.1095	--	--	2%	2%
9	0.2831	--	--	0.8139	--	--	3.8%	Not stated
10	0.0263	--	--	0.0757	--	--	1.6%	1.6%

[27] THD = Total Harmonic Distortion

[28] PWHD = Partial Weighted Harmonic Distortion

11	0.2338	--	--	0.6722	--	--	3.1%	3.1%
12	0.0197	--	--	0.0567	--	--	1.33%	1.33%
13	0.1740	--	--	0.5001	--	--	2%	2%
14	0.0224	--	--	0.0644	--	--	--	--
15	0.1457	--	--	0.4189	--	--	--	--
16	0.0164	--	--	0.0472	--	--	--	--
17	0.1083	--	--	0.3114	--	--	--	--
18	0.0130	--	--	0.0372	--	--	--	--
19	0.1020	--	--	0.2932	--	--	--	--
20	0.0104	--	--	0.0300	--	--	--	--
21	0.0810	--	--	0.2328	--	--	--	--
22	0.0079	--	--	0.0228	--	--	--	--
23	0.0699	--	--	0.2009	--	--	--	--
24	0.0110	--	--	0.0317	--	--	--	--
25	0.0573	--	--	0.1648	--	--	--	--
26	0.0113	--	--	0.0325	--	--	--	--
27	0.0544	--	--	0.1565	--	--	--	--
28	0.0092	--	--	0.0263	--	--	--	--
29	0.0407	--	--	0.1169	--	--	--	--
30	0.0110	--	--	0.0315	--	--	--	--
31	0.0358	--	--	0.1029	--	--	--	--
32	0.0128	--	--	0.0368	--	--	--	--
33	0.0306	--	--	0.0879	--	--	--	--
34	0.0103	--	--	0.0295	--	--	--	--
35	0.0286	--	--	0.0823	--	--	--	--
36	0.0098	--	--	0.0281	--	--	--	--
37	0.0203	--	--	0.0583	--	--	--	--
38	0.0127	--	--	0.0364	--	--	--	--
39	0.0167	--	--	0.0481	--	--	--	--

40	0.0143	--	--	0.0410	--	--	--	--
THD[29]	--	--	--	3.38	--	--	23%	13%
PWHD[30]	--	--	--	8.09	--	--	23%	22%

3. Power Quality – Voltage fluctuations and Flicker:

For **Power Generating Module**s of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For **Power Generating Module**s of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC P28.

The standard test impedance is 0.4 Ω for a single phase **Power Generating Module** (and for a two phase unit in a three phase system) and 0.24 Ω for a three phase **Power Generating Module** (and for a two phase unit in a split phase system). Please ensure that both test and standard impedance are completed on this form. If the test impedance (or the measured impedance) is different to the standard impedance, it must be normalised to the standard impedance as follows (where the **Power Factor** of the generation output is 0.98 or above):

d max normalised value = (Standard impedance / Measured impedance) x Measured value.

Where the **Power Factor** of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the standard impedance.

The stopping test should be a trip from full load operation.

The duration of these tests needs to comply with the particular requirements set out in the testing notes for the technology under test.

The test date and location must be declared.

Test start date	22.Mar.2023	Test end date	22.Mar.2023
Test location	Ginlong Technologies Co., Ltd.		

	Starting			Stopping			Running	
	d max	d c	d(t)	d max	d c	d(t)	P st	P lt 2 hours
Measured Values at	0.512%	0.037%	0	0.065%	0.046%	0	0.063	0.063

[29] THD = Total Harmonic Distortion

[30] PWHD = Partial Weighted Harmonic Distortion

test impedance								
Normalised to standard impedance	0.512%	0.037%	0	0.065%	0.046%	0	0.063	0.063
Normalised to required maximum impedance	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	0.65

Test Impedance	R	0.4	Ω	Xl	0.25	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	Xl	0.15 * 0.25 ^	Ω
Maximum Impedance	R	N/A	Ω	Xl	N/A	Ω

* Applies to three phase and split single phase **Power Generating Module**s. Delete as appropriate.

^ Applies to single phase **Power Generating Module** and **Power Generating Module**s using two phases on a three phase system. Delete as appropriate.

4. Power quality – DC injection: The tests should be carried out on a single **Generating Unit**. Tests are to be carried out at three defined power levels ±5%. At 230 V a 8 kW single phase **Inverter** has a current output of 34.8 A so DC limit is 87mA. These tests should be undertaken in accordance with Annex A.7.1.4.4.

The % DC injection ("as % of rated AC current" below) is calculated as follows:

% DC injection = Recorded DC value in Amps / Base current

where the base current is the **Registered Capacity** (W) / Vphase. The % DC injection should not be greater than 0.25%.

Test power level	10%	55%	100%
Recorded DC value in Amps	0.0288	0.0345	0.0412
as % of rated AC current	0.083%	0.099%	0.118%
Limit	0.25%	0.25%	0.25%

5. Power Factor: The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity** and the measured **Power Factor** must be

greater than 0.95 to pass. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Note that the value of voltage stated in brackets assumes a **LV** connection. This should be adjusted for **HV** as required.

Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	0.9995	0.9995	0.9994
Power Factor Limit	>0.95	>0.95	>0.95

6. Protection – Frequency tests: These tests should be carried out in accordance with the Annex A.7.1.2.3. For trip tests, frequency and time delay should be stated. For “no trip tests”, “no trip” can be stated.

Function	Setting		Trip test		“No trip tests”	
	Frequency	Time delay	Frequency	Time delay	Frequency /time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.48Hz	20.024s	47.7 Hz 30 s	Yes
U/F stage 2	47 Hz	0.5 s	47.01Hz	0.542s	47.2 Hz 19.5 s	Yes
					46.8 Hz 0.45 s	Yes
O/F	52 Hz	0.5 s	52.01Hz	0.534s	51.8 Hz 120.0 s	Yes
					52.2 Hz 0.45 s	Yes

Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The “No trip tests” need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

7. Protection – Voltage tests: These tests should be carried out in accordance with Annex A.7.1.2.2. For trip tests, voltage and time delay should be stated. For “no trip tests”, “no trip” can be stated.

Note that the value of voltage stated below assumes a **LV** connection This should be adjusted for **HV** taking account of the VT ratio as required.

Function	Setting		Trip test		“No trip tests”	
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V	0.8 pu (184 V)	2.5 s	184.2V	2.548s	188 V 5.0 s	Yes

					180 V 2.45 s	Yes
O/V stage 1	1.14 pu (262.2 V)	1.0 s	262.0V	1.042s	258.2 V 5.0 s	Yes
O/V stage 2	1.19 pu (273.7 V)	0.5 s	273.4V	0.540s	269.7 V 0.95 s	Yes
					277.7 V 0.45 s	Yes

Note for Voltage tests the Voltage required to trip is the setting ±3.45 V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting ±4 V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8.Protection – Loss of Mains test: These tests should be carried out in accordance with BS EN 62116. Annex A.7.1.2.4.

The following sub set of tests should be recorded in the following table.

Test Power and imbalance	33% -5% Q Test 22	66% -5% Q Test 12	100% -5% P Test 5	33% +5% Q Test 31	66% +5% Q Test 21	100% +5% P Test 10
Trip time. Limit is 0.5s[31]	0.312s	0.306s	0.286s	0.336s	0.303s	0.318s

Loss of Mains Protection, Vector Shift Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the **Power Generating Module** does not trip under positive / negative vector shift.

	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49.5Hz	+50 degrees	Yes
Negative Vector Shift	50.5Hz	- 50 degrees	Yes

[31] If the device requires additional shut down time (beyond 0.5 s but less than 1 s) then this should be stated on this form.

Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6. Confirmation is required that the **Power Generating Module** does not trip for the duration of the ramp up and ramp down test.			
Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95 Hzs^{-1}	2.1 s	Yes
51.0 Hz to 49.0 Hz	-0.95 Hzs^{-1}	2.1 s	Yes
9. Limited Frequency Sensitive Mode – Overfrequency test: The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%. This test should be carried out in accordance with Annex A.7.1.3, which also contains the measurement tolerances.			
Active Power response to rising frequency/time plots are attached if frequency injection tests are undertaken in accordance with Annex A.7.2.4.			**Yes**
Alternatively, test results should be noted below:			

Test sequence at **Registered Capacity** >80%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	8030W	50.00Hz	8322W	-
Step b) 50.45Hz ±0.05Hz	7957W	50.45Hz		-
Step c) 50.70Hz ±0.10Hz	7558W	50.70Hz		-
Step d) 51.15Hz ±0.05Hz	6828W	51.15Hz		-
Step e) 50.70Hz ±0.10Hz	7551W	50.70Hz		-
Step f) 50.45Hz ±0.05Hz	7956W	50.45Hz		-
Step g) 50.00Hz ±0.01Hz	8027W	50.00Hz		48kW/min
Test sequence at **Registered Capacity** 40% - 60%	Measured **Active Power** Output	Frequency	Primary Power Source	**Active Power** Gradient
Step a) 50.00Hz ±0.01Hz	4019W	50.00Hz		-
Step b) 50.45Hz ±0.05Hz	3932W	50.45Hz		-

Step c) 50.70Hz ±0.10Hz	3534W	50.70Hz	3133W	-
Step d) 51.15Hz ±0.05Hz	2829W	51.15Hz		-
Step e) 50.70Hz ±0.10Hz	3532W	50.70Hz		-
Step f) 50.45Hz ±0.05Hz	3936W	50.45Hz		0kW/min
Step g) 50.00Hz ±0.01Hz	4008W	50.00Hz		48kW/min

10. Protection – Re-connection timer

Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1. Both the time delay setting and the measured delay should be provided in this form; both should be greater than 20 s to pass. Confirmation should be provided that the **Power Generating Module** does not reconnect at the voltage and frequency settings below; a statement of "no reconnection" can be made.

Time delay setting	Measured delay	Checks on no reconnection when voltage or frequency is brought to just outside stage 1 limits of Table 10.1.			
30s	42s	At 1.16 pu (266.2 V **LV** connection, 127.6 V **HV** connection assuming 110 V ph-ph VT)	At 0.78 pu (180.0 V **LV** connection, 85.8 V **HV** connection assuming 110 V ph-ph VT)	At 47.4 Hz	At 52.1 Hz
Confirmation that the **Power Generating Module** does not re-connect.		Yes	Yes	Yes	Yes

11. Fault level contribution: These tests shall be carried out in accordance with EREC G99 Annex A.7.1.5. Please complete each entry, even if the contribution to the fault level is zero.

For **Inverter** output		
Time after fault	Volts	Amps
20ms	52.6V	43.39A
100ms	51.0V	0
250ms	51.6V	0
500ms	61.3V	0
Time to trip	0.045s	In seconds

12. Self-Monitoring solid state switching: No specified test requirements. Refer to Annex A.7.1.6.	
It has been verified that in the event of the solid state switching device failing to disconnect the **Power Park Module**, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	N/A (Solid state switch means electronic switch, Solis inverter uses mechanical dual relay protection with relay checks, which drops the voltage below 50V in 0.5s)
13. Wiring functional tests: If required by para 15.2.1.	
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	N/A (Not applicable. Refer to 15.2.1, inverter is using special connector for wiring)
14. Logic interface (input port)	
Confirm that an input port is provided and can be used to shut down the module	Yes. (Logic interface is marked as “DRM” on inverter. Please see inverter manual part 4.3.9 for detail.)
Provide high level description of logic interface, e.g. details in 11.1.3.1 such as AC or DC signal (the additional comments box below can be used)	Yes. (Logic interface marked “DRM” on inverter which can be operated by a simple switch or contactor. When the switch is closed the inverter can operated normally. When the switch is opened, the inverter will reduce it’s output power to zero within 5s. The signal from the inverter that is being switched is DC about 10 V.)
15. Cyber security	
Confirm that the **Power Generating Module** has been designed to comply with cyber security requirements, as detailed in 9.1.7.	Yes(The inverter can work with S2-WL-ST data logger to meet the requirements of ENSI EN 303645)
Additional comments.	
The test result is based on S6-GR1P8K2. All the series of inverters electrical character are the same. So the test result can cover all other models.	